

OSTİM TECHNICAL UNIVERSITY

**2024-2025 SEMESTER**

**ELECTRICAL-ELECTRONIC ENGINEERING DEPARTMENT GRADUATION PROJECT PROPOSAL FORM**

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| **Lecture Code: EEE400** | **Lecture Name: Graduation Project** |
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| **Project Title / Number of Students:** | **FPGA Applications****(2 people)**  |
| **WORKS AND PROCEDURES TO BE DONE IN THE PROJECT****(Put the item number on the left and write it in order)** |
| **Item**1. **Hardware description using Verilog**
2. **FPGA implementation (Tang Nano 9K, Basys3 etc)**
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| PROJECT AIMS |
| **Item**1. **FPGA applications based on Digital Systems Design Lecture**
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| **SUPERVISOR** |
| TitleAssist. Prof. Dr.  | Name SurnameŞ. GÜLGÖNÜL | Signature |