grafik, yazı tipi, grafik tasarım, logo içeren bir resim

Açıklama otomatik olarak oluşturuldu

OSTİM TECHNICAL UNIVERSITY

**2024-2025 SEMESTER**

**ELECTRICAL-ELECTRONIC ENGINEERING DEPARTMENT GRADUATION PROJECT PROPOSAL FORM**

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| **Lecture Code: EEE400** | **Lecture Name: Graduation Project** | | |
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| **Project Title / Number of Students:** | **FPGA Applications**  **(2 people)** |
| **WORKS AND PROCEDURES TO BE DONE IN THE PROJECT**  **(Put the item number on the left and write it in order)** | |
| **Item**   1. **Hardware description using Verilog** 2. **FPGA implementation (Tang Nano 9K, Basys3 etc)** | |
| PROJECT AIMS | |
| **Item**   1. **FPGA applications based on Digital Systems Design Lecture** | |

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| **SUPERVISOR** | | |
| Title  Assist. Prof. Dr. | Name Surname  Ş. GÜLGÖNÜL | Signature |